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February 17, 2004

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Edward A. Ramsden
Assignee: Lattice Semiconductor Corporation
Title: Analog-To-Digital Converters
Serial No.: 10/687,468
Examiner: Unknown
Docket No.: M-15273 US
Filed: 10/15/2003
Group Art Unit: 2816

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate); and
- (3) Submission of formal drawings with three (3) sheets of formal drawings including Figures 1, 2, and 3.

- ☒ No additional fee is required.
☐ The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After</u> <u>Amendment</u>		Highst No. Previously Paid <u>For</u>		Present <u>Extra</u>		<u>Rate</u>		Additional <u>Fee</u>
Total Claims	21	Minus	21	=	0	x	\$18.00	\$	0
Independent Claims	3	Minus	3	=	0	x	\$86.00	\$	0
<input type="checkbox"/>	Fee of ____ for the first filing of one or more multiple dependent claims per application								\$
<input type="checkbox"/>	Fee for Request for Extension of Time								\$

Total additional fee for this Amendment:

\$

- ☒ Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.
- ☐ Please charge our Deposit Account No. 50-2257 in the amount of \$
- ☒ Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257

Total: \$

0

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 17, 2004.

Eric Hoover

February 17, 2004
Date of Signature

Respectfully submitted,

Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Edward A. Ramsden
Assignee: Lattice Semiconductor Corporation
Title: Analog-To-Digital Converters
Serial No.: 10/687,468 Filing Date: 10/15/2003
Examiner: Unassigned Group Art Unit: 2816
Docket No.: M-15273 US

Irvine, California
February 17, 2004

Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit three (3) sheets of formal drawings, consisting of Figures 1, 2, and 3, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on February 17, 2004.

Eric Hoover

February 17, 2004

Respectfully submitted,

Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940

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